

# Contact Profile (1/2)

- **Patrizia Asirelli, Maurice ter Beek, and Stefania Gnesi @ ISTI-CNR, Pisa, Italy.**
- **Significant experience in developing techniques and tools for the formal specification and verification, through model checking, of distributed and concurrent systems. Recent focus on developing formal methods and tools (in particular suitable logics) for SPLE.**

# Contact Profile (2)

- **Objectives / Looking for**
  - We look for feedback on our technique's applicability to current industrial problems.
  - We would like to meet PL engineers from industrial domains which use model-based development of safety-critical systems.
  - Our participation is a success if we indeed obtain useful feedback on our technique's applicability to current industrial problems.

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# Current Solution

Model: MTS/CCS; Logic: vaCTL; Tool: FMC/VMC

The screenshot shows the FMC v5.0b web interface. The browser address bar displays the URL `http://fmlab.isti.cnr.it/fmc/V5.0/fmc.html`. The interface is divided into several sections:

- Top Left:** "FMC v5.0b" with five black circles below it and a landscape painting.
- Top Right:** A list of transitions (T0 to T8) and a network definition:
 

```
T0 = must(free_left).T0 + must(access_request).T1
T1 = must(free_right).T2
T2 = may(LC_close_command).T3 + may(skip).T4 + may(light_on).T4
T3 = may(blink_light).T3 + must(LC_closed).T4
T4 = must(signal_to_green).T5
T5 = must(track_circuit_occupied).T6
T6 = must(signal_to_red).T7
T7 = must(track_circuit_free).T8
T8 = may(LC_open_command).T0 + may(skip).T0 + may(light_off).T0

net SYS = T0
```
- Bottom Left:** A "Commands Menu" with buttons for "New Model ..." and "Load Current Model". Below it is a collage of electronic components.
- Bottom Center:** A section titled "Logical Formula" containing:
 

```
not E [true
  {not (may(signal_to_green) or must(signal_to_green))} U
  {must(track_circuit_occupied)} true]
```
- Bottom Right:** Two buttons: "Check The Formula" and "Explain the Result".

# Why is it interesting?

We propose a formal framework: vaCTL logic and its natural interpretation structure (MTSs). Product derivation is defined in the framework. Logic formulae are used for constraints as well as properties, over families and products alike.

MTSs not to be used directly by PL engineers: it is the underlying semantic model. In practice, behavioral descriptions in high-level formalisms used in model-based development (e.g. UML).

vaCTL hidden from PL engineers by providing template formulae (i.e. temporal logic patterns).

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# Potential Collaborations

Maybe our experience in managing variability can help realizing and/or evaluating Complex Changes in Variability Models.

(We would also have liked to see whether our framework can be applied to the NASA Goddard Space Flight Center's Core Flight System SPL.)

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